

eDAS Block Diagram - Figure 1

Shaded blocks are all handled in software within the microprocessor

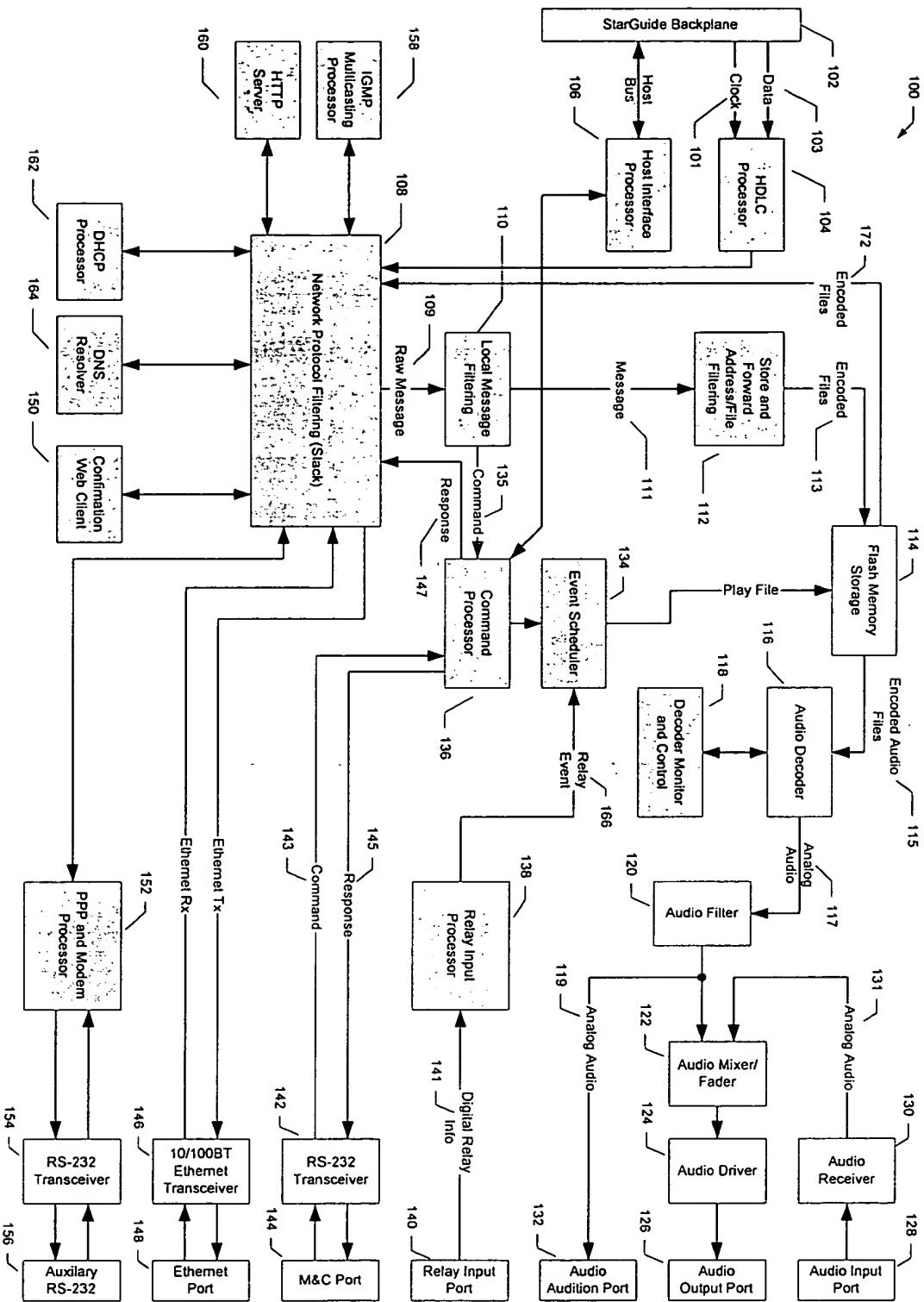


Figure 2. Hardware Block Diagram

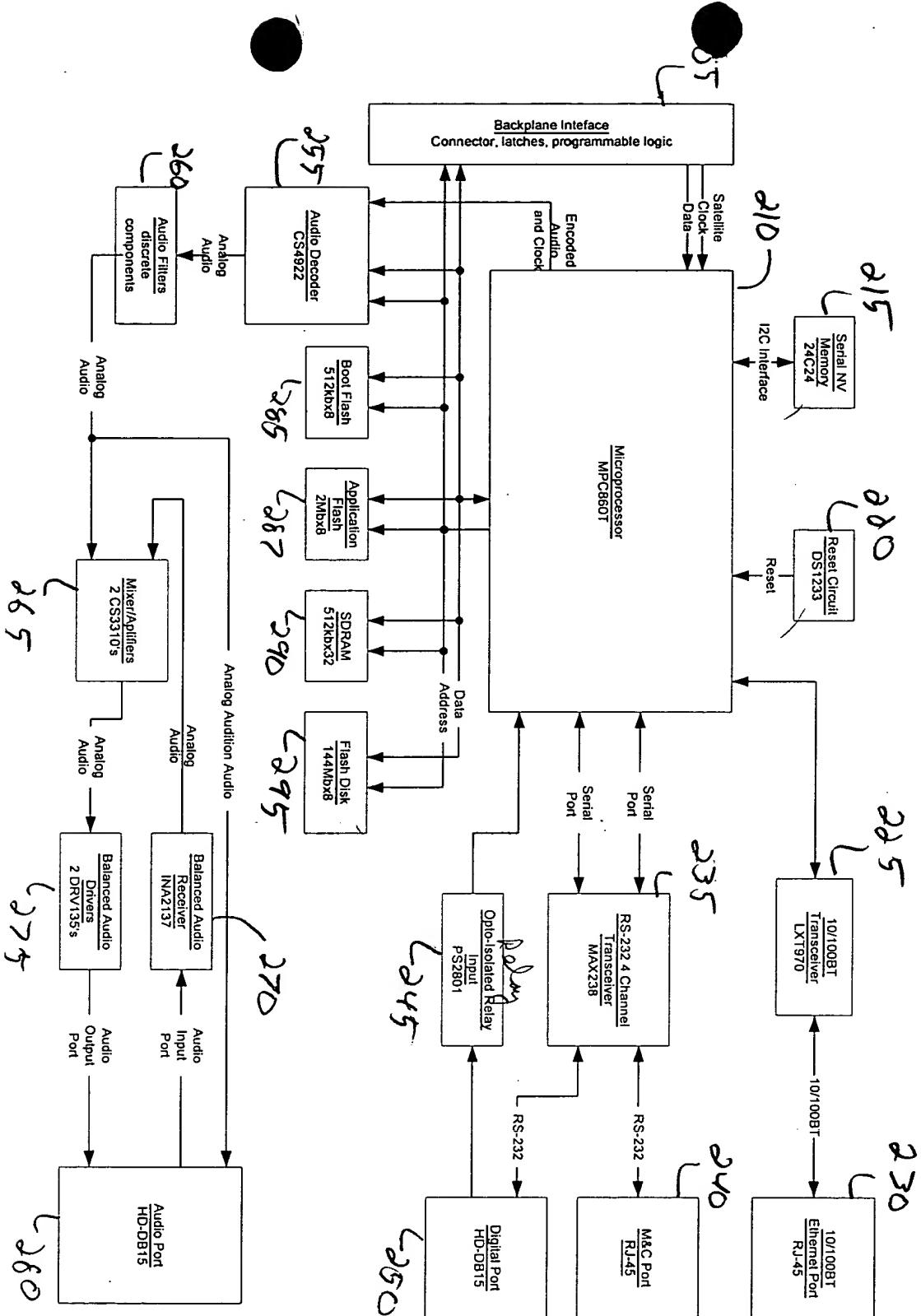
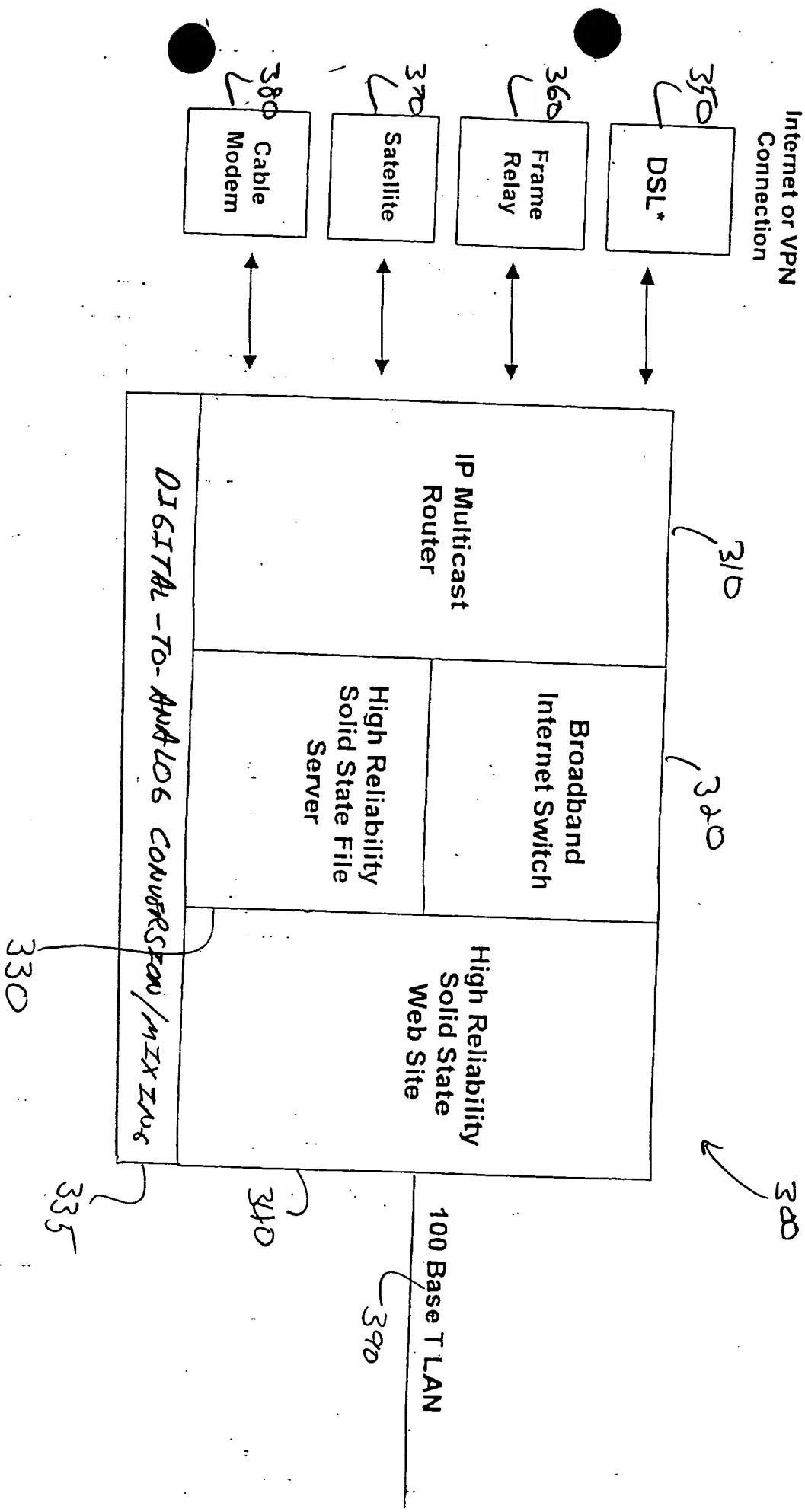


FIG. 3

StarGuide StarGate



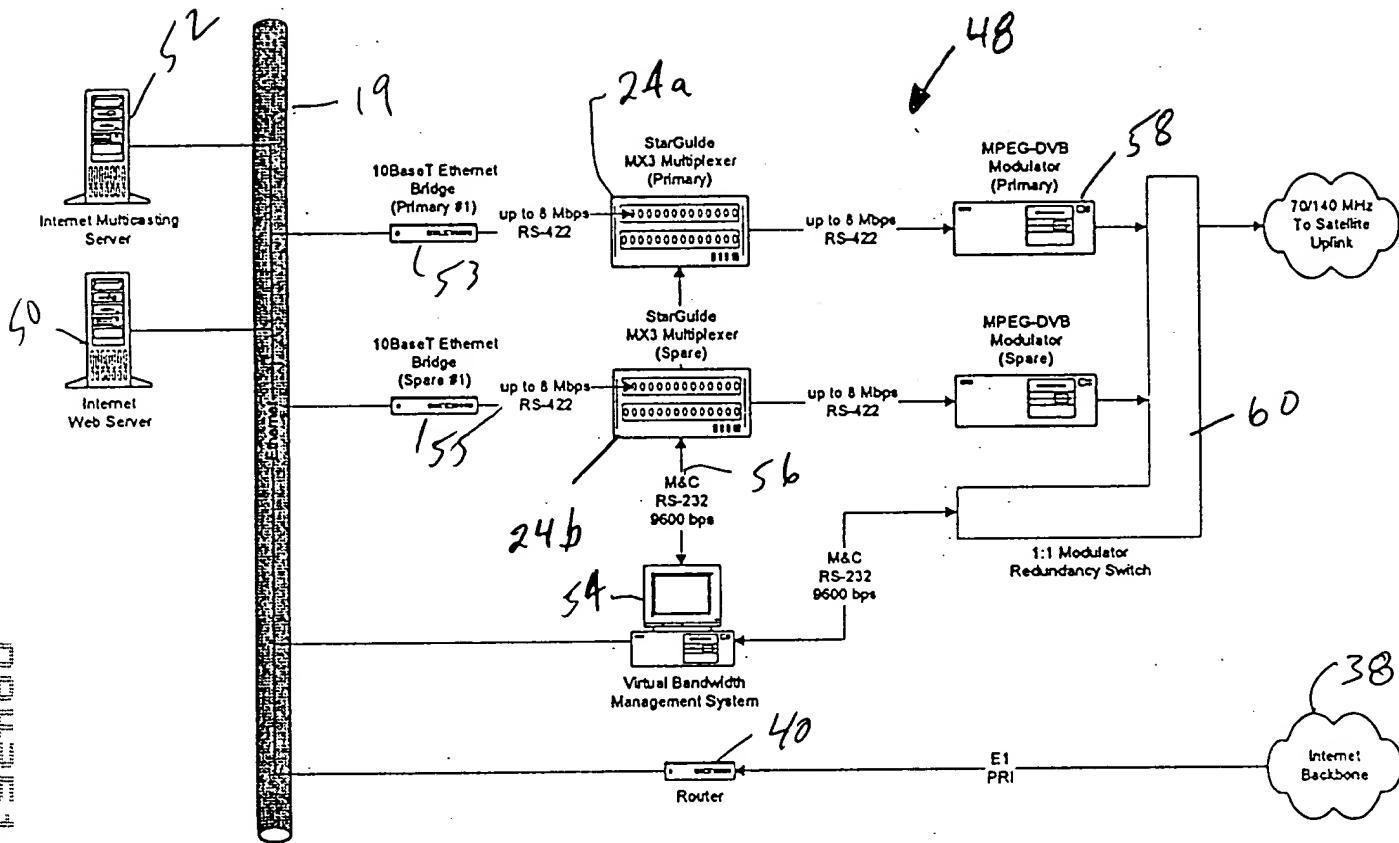


Fig 4

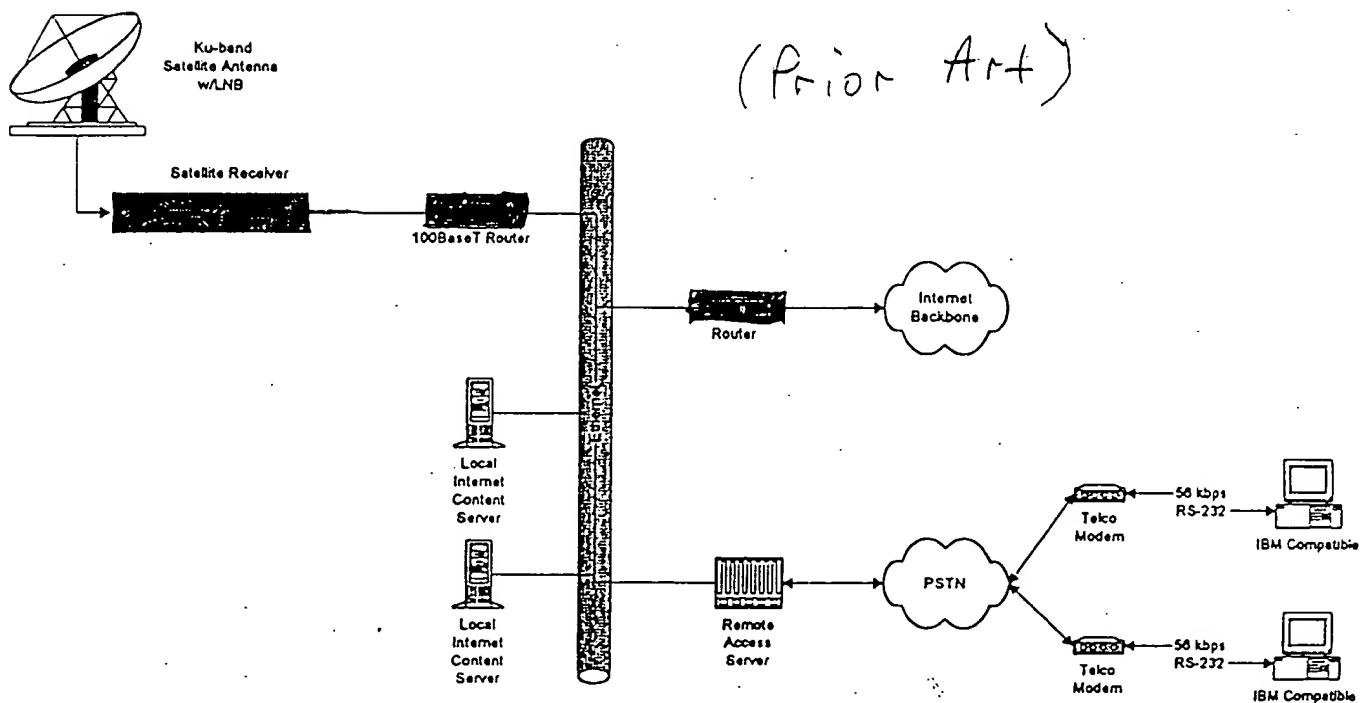


Fig 10

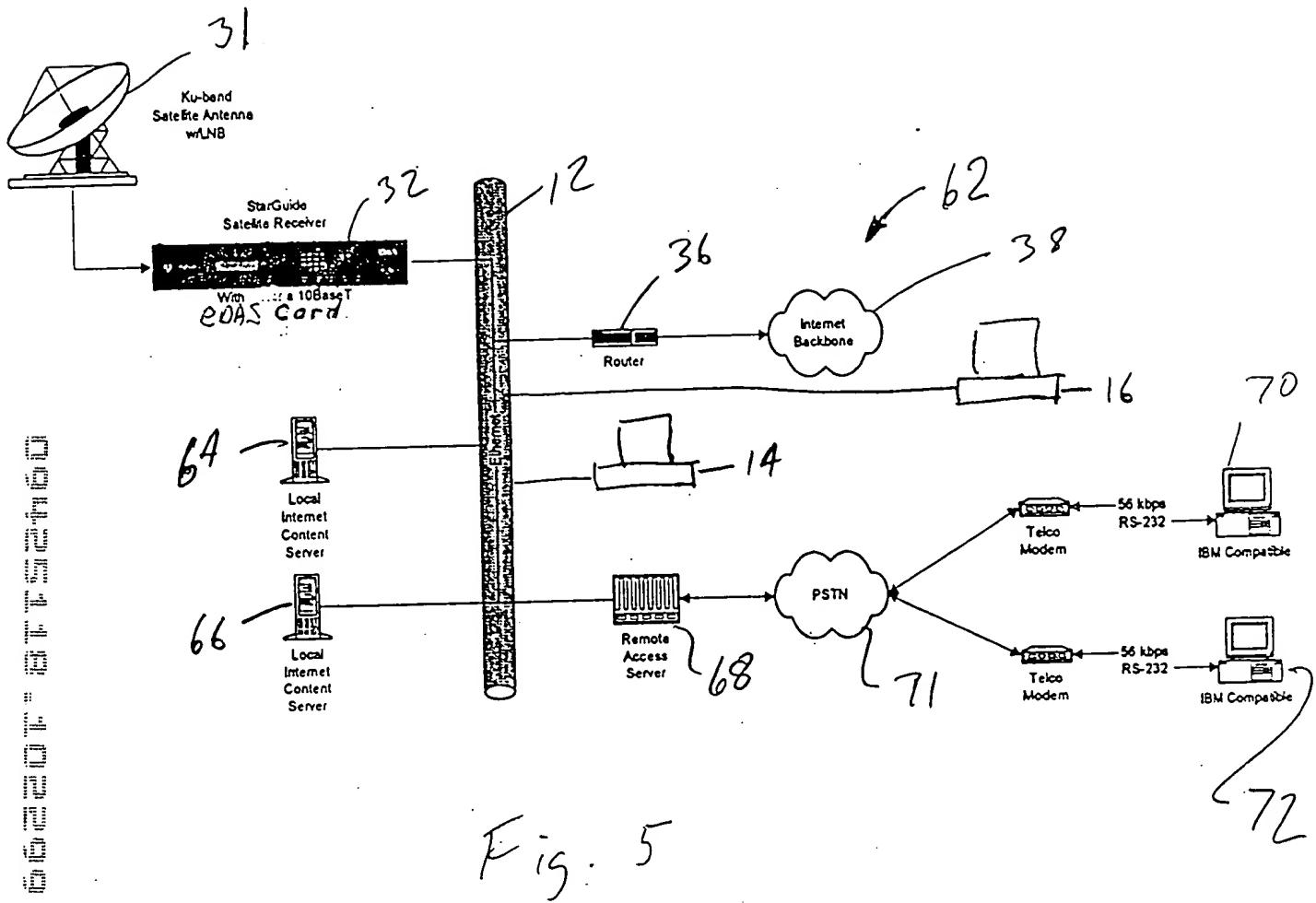


Fig. 5

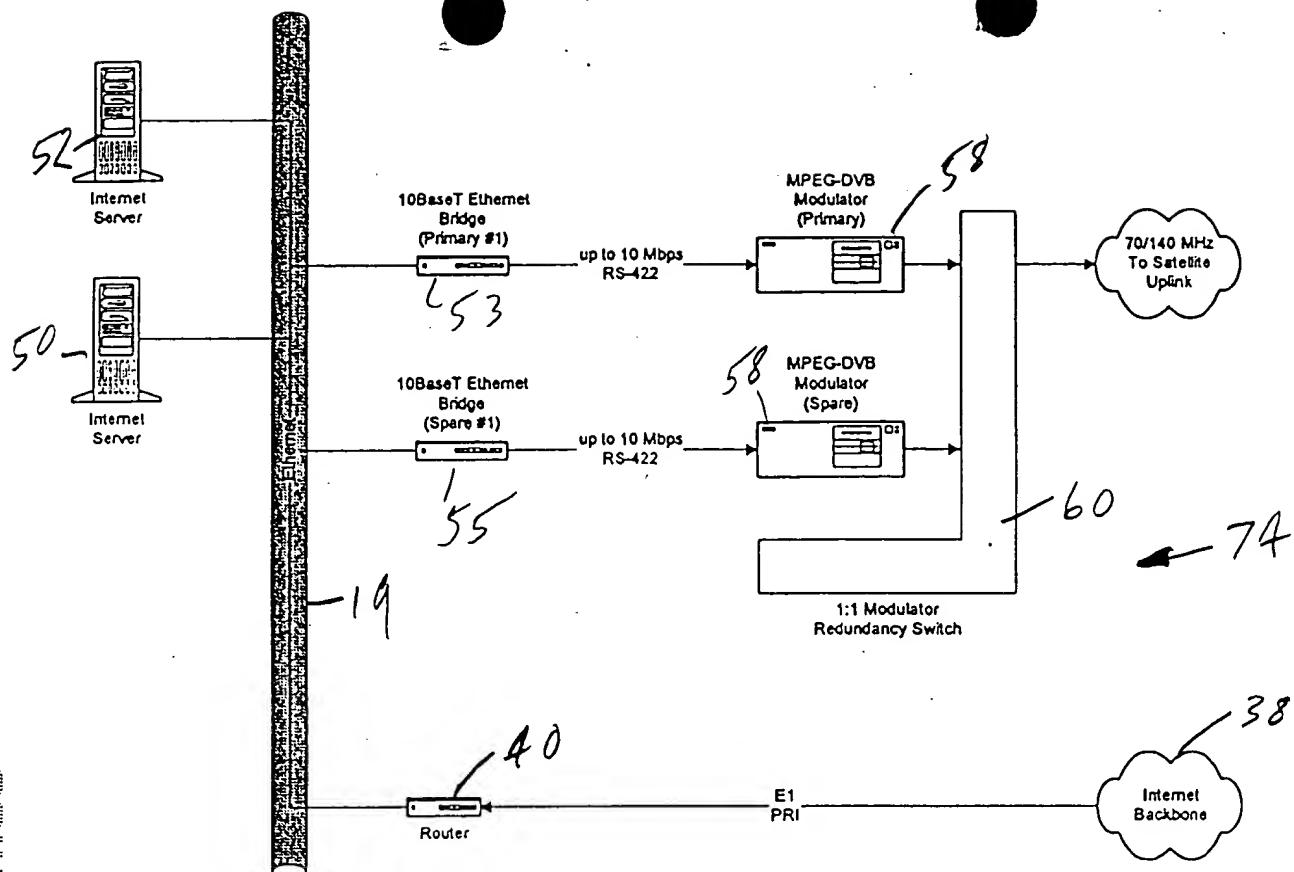


Fig. 6

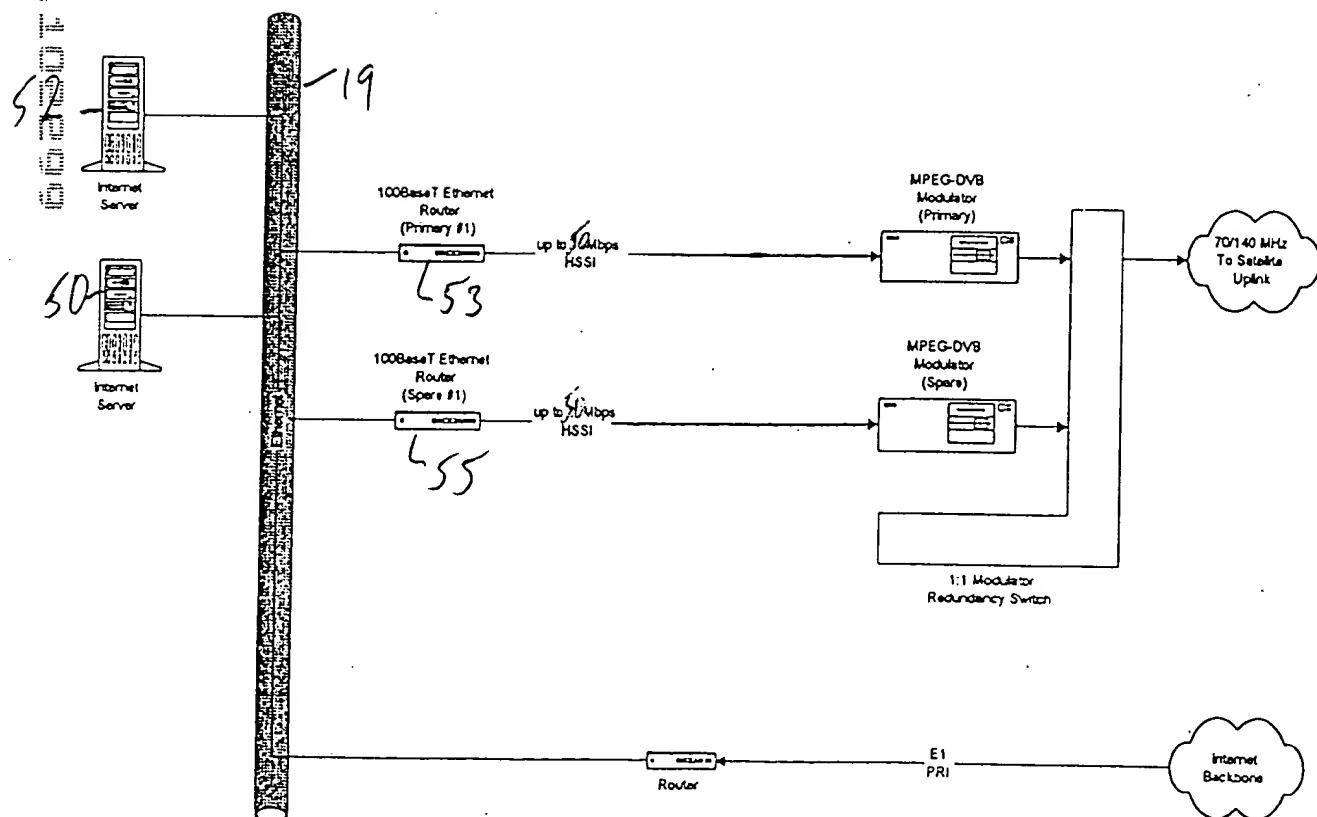


Fig. 7

Data Flow Using Internet Backchannel

Figure 8

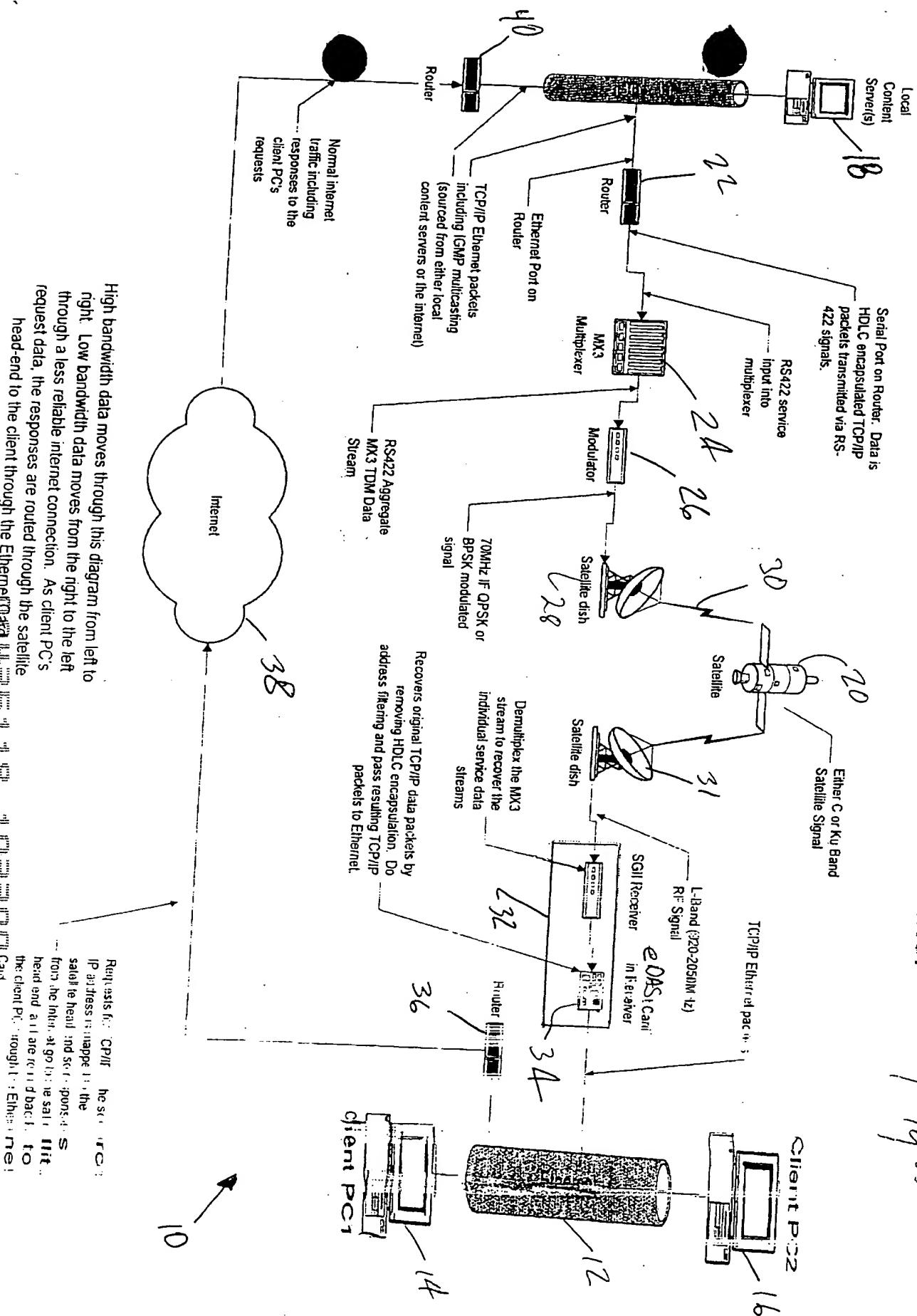
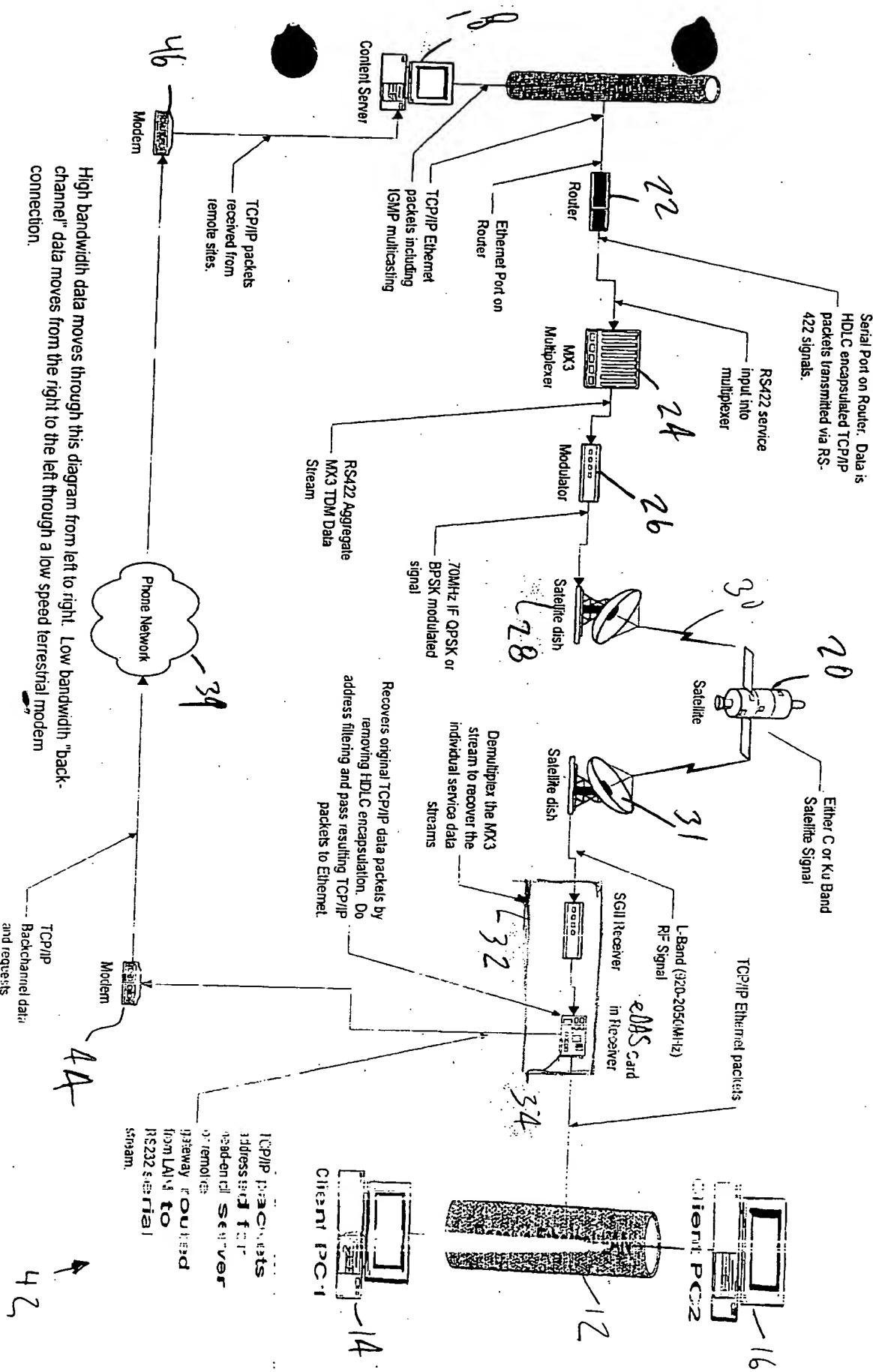


Figure 9 Closed-Loop Network (Intranet) Capable of Delivering TCP/IP Data Using External Telco Modem Backchannel



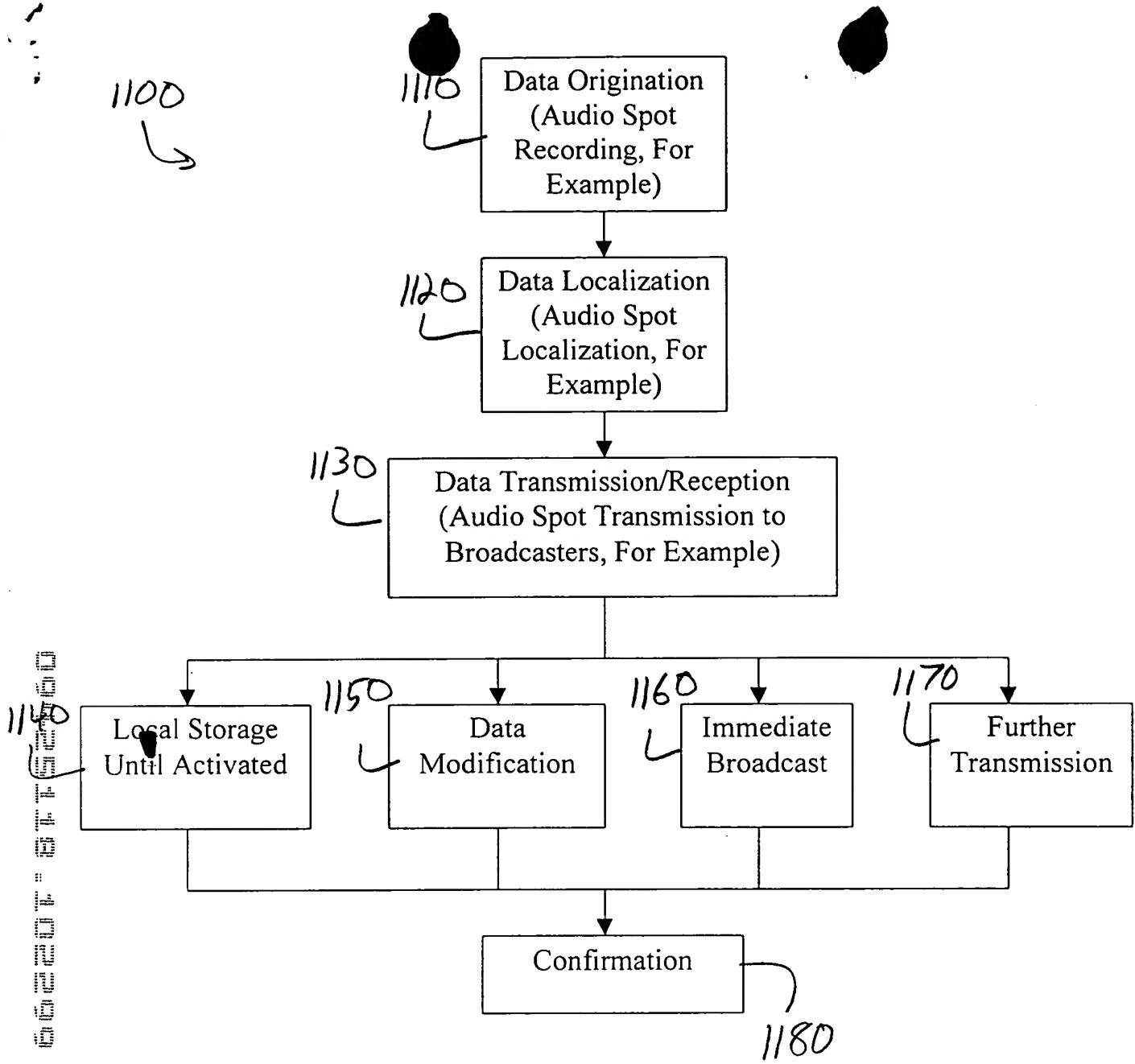


Figure 11